



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/671,229
Filing Date September 24, 2003
Inventor..... Leonard Forbes
Assignee..... Micron Technology, Inc.
Group Art Unit..... 2814
Examiner L. Pham
Customer No. 021567
Attorney's Docket No. MI22-2272
Title: Electronic Apparatus, Silicon-on-Insulator Integrated Circuits, and Fabrication
Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference -See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed together with the filing of a Request for Continued Examination (RCE) Application. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

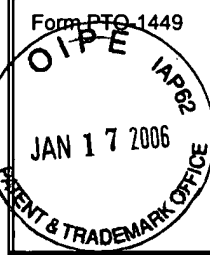
Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 17 Jan 2006

By:

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Form PTO 1449 		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2272		SERIAL NO. 10/671,229	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT: Leonard Forbes			
U.S. PATENT DOCUMENTS				FILING DATE September 24, 2003		GROUP 2814	
*Examiner's Initials	AA	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	2002/0135549 A1	09/2002	Kawata, Hirotaka			
	AB						
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	AD						
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FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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